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21JUL03 E8

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1. Your reference

597GB

2. Patent application number (The Patent Office will fill in this part)

0316921.6

19 JUL 2003

3. Full name, address and postcode of the or of each applicant (underline all surnames)

Patents ADP number (if you know it)

If the applicant is a corporate body, give the country/state of its incorporation

Renishaw plc New Mills Wotton-under-Edge Gloucestershire, GL12 8JR

2691002

United Kingdom

4. Title of the invention

Reader For A Scale Marking

5. Name of your agent (if you have one)

"Address for service" in the United Kingdom to which all correspondence should be sent (including the postcode)

R D Cavill et al

Renishaw plc, Patent Department New Mills Wotton-under-Edge Gloucestershire GL12 8JR

Patents ADP number (if you know it)

7908650661

6. If you are declaring priority from one or more earlier patent applications, give the country and the date of filing of the or of each of these earlier applications and (if you know it) the or each application number

Country

Priority application number (if you know it)

Date of filing (day / month / year)

If this application is divided or otherwise derived from an earlier UK application, give the number and the filing date of the earlier application

Number of earlier application

Date of filing (day / month / year)

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READER FOR A SCALE MARKING

The present invention relates to a detector for detecting a reference mark or the like, in particular but not exclusively during measurement operations which use a scale and a scale reader.

The detection of reference marks is disclosed in our prior patent publication no. PCT/GB02/00638, the disclosure of which is incorporated herein by reference.

Reference marks allow a reader to find its position and are usually provided separately to periodic scale marks which provide an incremental signal to the reader for determining displacement. A reference mark could be anything that is recognisable by the reader. In practice a pattern is used because it is simple to produce and is more reliable, producing fewer false readings. When a pattern is used the reader will have a similar pattern so that when the mark and reader align a stronger signal is provided by the reader.

Combined reference and scale marks have been used

commercially. In such instances the reference mark may be formed by a pattern of missing bits of the scale. The incremental signal from the periodic scale is read over a portion of the scale large enough to compensate for missing bits of the scale.

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In order to improve accuracy known scale readers have employed two detectors e.g. photodiodes, usually called split detectors, to read a scale. The two photodiodes have been offset one relative to the other in the

direction of measurement by a small amount. When this configuration has been used the output of one photodiode lags behind (or is in front of) the other. A known technique is to produce a zero crossing output (detailed below) from the two photodiodes which improves detection accuracy.

The invention provides a reference mark reader having a patterned array of detectors for detecting a reference

10 mark on a measurement scale wherein each of the detectors has an output, two or more of the detector outputs are summed to provide a first summed output and others of the detector outputs are summed to provide a second summed output, and the second summed output is subtracted from the first summed output.

In one preferred form the array of detector comprises two sets of detectors, however, there are drawbacks to using two sets of detectors: there is an increased cost; alignment of the two sets is difficult during manufacture; and misalignment between the reader and the reference mark is a problem during use.

The present invention provides also a reference mark

25 reader having a single patterned array of detectors and
a zero crossing output.

In this way a zero crossing output can be obtained from a single row of detectors.

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The detectors may be photodiodes or a photodiode block. The first and second summed outputs may be subtracted by means of a difference amplifier. The outputs of the detectors may each be converted into digital form and

summed digitally.

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Embodiments of the invention will now be described with reference to the accompanying drawings, in which:

Figs 1a and b show a reference mark reader arrangement according to the invention;

Figs 2a,b and c show another reference mark reader arrangement according to the invention;

Fig 3 shows an electrical schematic diagram of the 10 components used in the reader shown in Figs 2a,b and c;

Figs 4,5,6 and 7 show alternative reference mark reader arrangements.

Fig 1a shows a novel arrangement of a reference mark reader 20 and a reference mark rm incorporated into an incremental scale 10.

In this arrangement a scale 10 has periodic markings of alternating transparent 12 and opaque 14 areas, e.g.

20 chrome on glass. These markings are used in a conventional way to produce a signal indicative of displacement, using a conventional reader (not shown) adjacent the reference mark detector 20. In this arrangement a scale can move in the direction of arrow x and, in practice, its opposite direction also. For simplicity travel in the x direction only is described.

Incorporated into the periodic scale markings 12 and 14 is a reference mark rm. In this example the mark is 7 periods long. The reference mark could be completely separate from the incremental scale in which case any size of pattern could be used. The reference mark reader 20 has a point light source 22, light from which propagates through the transparent areas 12 and onto

rows of detectors A and B. Each row of detectors is formed from 7 photodiodes 1-7. The light will fall with little effect on the detectors as the scale is moved relative to the detectors. The reference mark has a pattern 1100101 where "1" represents a transparent area 12 and "0" represents an opaque area Therefore, detectors 1,2,5 and 7 in each row only 14. are monitored, which corresponds to the illuminated detectors when the reference mark 1100101 coincides with the corresponding pattern formed by those 10 monitored detectors.

When the reference mark coincides with each detector, rather than substantially ineffectual light falling on those detectors, each detector is fully illuminated and 15 a sudden increase in output $V_{\mathtt{A}}$ and $V_{\mathtt{B}}$ is effected.

Detector rows A and B are staggered by one detector width in this instance, so signal peaks $V_{\mathtt{A}}$ and $V_{\mathtt{B}}$ are staggered also. To improve accuracy signal $V_{\mbox{\scriptsize B}}$ is subtracted from signal $V_{\mathtt{A}}$ at difference amplifier 24 to give an overall output signal Vour. It will be noted that V_{OUT} crosses zero at a distinct point and this zero-crossing can be used to give accurate positional 25 information.

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In this embodiment a mask can be placed over the detectors and all detectors can be monitored. modification will achieve the same results as the arrangement illustrated. Alternatively, the unmonitored detectors 3,4 and 6 could be left out altogether.

Whilst the arrangement shown in Figs 1a and 1b is

satisfactory, it could be improved. Figs 2a,b and c illustrate an improved embodiment of the invention.

The components used in Figs 2a,b and c are similar to those illustrated in Figs 1a and b, however, only one 5 row of detectors 20' is used in Figs 2a,b and c. reference mark 1100101 is used again but the way in which the corresponding detectors are monitored is different. The reference mark pattern is written in 10 the second row of the table illustrated in Fig 2c. next row in this table has the same pattern displaced one place to the left (it could be more than one place and could be displaced to the right). The two rows are subtracted one column at a time and the result is given 15 in the lowest row. This result dictates the pattern of the detectors. Now, a "1" detector (i.e. detectors 3,6 and 8) is connected to output A and a "-1" detector (i.e. detectors 1,5 and 7) is connected to output B. The resultant output of these connections when the 20 1100101 reference mark is detected is the outputs $V_{\mathtt{A}}$ When the outputs are subtracted a zerocrossing signal V_{OUT} is obtained again.

Since only one single row of detectors is used in this embodiment the reader is less susceptible to yaw misalignment and is cheaper to produce.

Fig 3 shows a simplified electrical schematic diagram of one way in which the signals from the detectors 20' of Figs 2a and b can be processed. The same scheme could be used for the arrangement shown in Figs 1a and b also. Photodetector diodes D1 to D8 are shown. Diodes D3,D6 and D8 are connected to the input of an amplifier 26A for turning their current outputs into a

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voltage V_A . Diodes D1,D5 and D7 are likewise connected to an amplifier 26B. The outputs of the amplifiers 26A and 26B are connected to the input of difference amplifier 24 which subtracts V_B from V_A to produce an output signal V_{OUT} . Diodes D2 and D4 are not used and are connected to OV.

The amplitude of the V_{OUT} signal is used in this instance to activate a further circuit (not shown) which produces a signal when V_{OUT} crosses zero.

In addition to the difference amplifier 24 a summing amplifier 28 is shown. This amplifier sums the voltages V_A and V_B to produce a voltage VC_{OUT}. V_A and V_B will be at a maximum when all the respective photodiodes are illuminated i.e. when a solid block of "1"s (in this instance) is encountered by the detector. VC_{OUT} can be used to detect control marks on the scale e.g. end stops. Other values of VC_{OUT} e.g. zero, ½, ¾ of maximum can be used to indicate other control marks.

Fig 4 shows an alternative arrangement for processing the signals produced by the detectors 20'. Voltage signals V_A and V_B are fed into two respective analogue-to-digital converters (A-D) 30A and 30B. These A-Ds in turn feed a microprocessor 34 which gives a reference pulse output. The microprocessor might be used to convert the voltage signals V_A and V_B into digital form without the need for the A-Ds 30A and 30B.

Figs 5,6 and 7 show alternative configurations of detector 20 and scale 10. In Fig 5 a reflective scale 10 is used and a light source 22 is to one side of the detector block 20. The reference mark could be

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reflective and the scale background non-reflective or vice versa. The light 22 could be in the middle of the block 20.

Fig 6 shows a collimated light source producing light which passes through a transparent reference mark on scale 10. Fig 7 shows a diffuse light source 28 producing light which passes through a reference mark on scale 10 and an imaging device such as pinhole, slit or lens 26. From the imaging device the light propagates to the detector 20.

In Figs 5,6 and 7 the same detector arrangement as described above may be used.

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Many variants and modifications to the embodiments described and illustrated will be apparent. reference mark is illustrated as being incorporated into an incremental scale. However, it is possible 20 that a separate reference mark may be provided and the invention need not be used with an incremental scale. The reader may be moved whilst the scale is stationary or vice versa. Both may move relative to each other. The invention is described with reference to light and 25 a light source. The light could be a point source e.g. a laser diode, an area source e.g. an LED, diffuse light e.g. an LED array or ambient light. The light used could be convergent, divergent, collimated or The light may not be visible, it could be 30 infra-red light or some other part of the electromagnetic spectrum. The invention could be practised with detectors other than light detectors e.g. magnetic detectors for use with a magnetic reference mark may be employed. A patterned reference

mark having a pattern 1100101 is illustrated but any recognisable pattern could be used. The pattern need not extend in a linear manner as shown but could be 2-dimensional. In practice a 24 bit series of "1"s and "0"s representing light transmissive/reflective, and non-transmissive/non-reflective parts of a mark has proven satisfactory. One example of such a series is 11010101010110100100110011.











